Introduction

5.75" by 8" (just as big as your 5.25" floppy drive!). expansion cards. All these functions are built in a small form factor of Board Computer) functions as well as features usually found on embedded applications, and provides all the standard SBC (Single was specifically designed for industrial and high-end commercial compact, complete, high-performance, PC/AT compatible system. It The PCM-4860 is an all-in-one single board computer that provides a

interface, watchdog timer and keyboard connector. controller, two serial ports, a bi-directional parallel port, PS/2 mouse pin DRAM SIMM sockets, an IDE hard disk controller, a floppy disk The PCM-4860 comes equipped with a 486 CPU. It features two 72-

environment, the PCM-4860 also contains a 16-bit Ethernet interface capabilities with other devices, and enable inclusion in a LAN supports CRTs and flat-panel LCDs. To strengthen its communication The PCM-4860 also features an on-board VGA display controller that

your on-board SSD from tampering. solid-state its access time is much faster. It is also easier to protect mechanical counterpart, especially in hostile environments. Being an external drive possible. The SSD is much more reliable than its SSD can emulate a floppy drive. This makes automatic boot without Another special feature is an on-board Solid State Disk (SSD). The

to six PC/104 modules can be piggybacked onto the PCM-4860. PC/104 standard, PC/104 expansion connectors are also on-board. Up To meet the rapidly increasing demand for the embedded industrial

tion and reliability are critical factors commercial and industrial applications where size, power consumpexpandability make it an ideal cost/performance solution for high-end The PCM-4860's highly compact form, numerous features and

Specifications

Standard SBC functions

- 80486SX-25/33 MHz, 80486DX2-66 MHz 80486DX-25/33 MHz
- Bus interface: ISA (PC/AT) bus
- Data bus: 32 bit
- Processing ability: 32 bit
- Chipset: VIA VL82C486A
- RAM memory: 1 MB to 32 MB. Accepts 1, 4, 8 or 16 MB SIMMs (for 1, 2, 4, 8, 16 and 32 MB configuration)
- hard disk drives (enabled/disabled) IDE hard disk drive interface: Supports up to two IDE (AT bus)
- CMOS backup: CMOS data backup in DS12885Q, avoiding data
- Floppy disk drive interface:

Supports up to two floppy disk drives, 51/4" (360 KB and 1.2 MB) and/or 31/2" (720 KB, 1.44 MB and 2.88 MB) (BIOS enabled/disabled)

- Bi-directional parallel port: Configurable to LPT1, LPT2, LPT3
- compatible) with 16-byte FIFO buffer. Supports speeds up to 115 Serial ports: One serial RS-232 port and one serial RS-232/RS-Kbps. Ports can be individually configured as COM1, COM2, 422/RS-485 port, jumper selectable. Both with 16C550 UARTs (or COM3, COM4 or disabled
- Real-time clock/calendar:

lithium battery for 10 years of data retention Uses DS-12885Q RTC chip and quartz oscillator, powered by a

A SA TANDES OF THE PARTY OF THE

- Watchdog timer: Can generate a system reset or IRQ15. Software I/O ports hex 043 and 443 to control the watchdog timer enabled/disabled. The timer interval is 1.6 sec. Your program uses
- DMA controllers: 2 x 87C37A
- DMA channels: 7
- Interrupt controllers: 2 x 87C59A
- Interrupt levels: 15
- Keyboard/mouse connector: An 8-pin Connector supports standard PC/AT keyboards and PS/2 mouse
- Bus speed: 8 MHz
- Max. power requirements: +5 V @ 2 A
- Power supply voltage: +5 V (4.75 V to 5.25 V)
- Operating temperature: 32 to 140°F (0 to 60°C)
- **Board size:** 8.0" (L) x 5.75" (W) (203 mm x 146 mm)
- **Board weight:** 11 oz. (0.31 Kg.)
- EMI: Pending

PC/104 Bus expansion

- PC/104 connector: 16-bit PC/104 connector for expansion
- Driving capacity for six PC/104 modules

Ethernet Controller functions

- Ethernet controller: 16-bit, Novell 2000 NE compatible with network boot support
- 10Base-T on-board, 10BASE-2 and 10Base-5 (AUI) optional

Flat-panel/CRT VGA controller functions

- Flat-panel/CRT VGA controller: Supports LCD, EL, CRT and gas plasma flat-panel displays
- High-resolution display up to 1024 x 768 (16 colors), 640 x 480 and 800 x 600 (256 colors)
- 512 KB DRAM for high-speed memory access

Solid State Disk functions

- Three 32-pin sockets for on-board Flash/RAM/ROM SSD
- Supports EPROM, EEPROM and Flash memory
- Up to 1.5 MB memory capacity
- Utility software included

Locating components

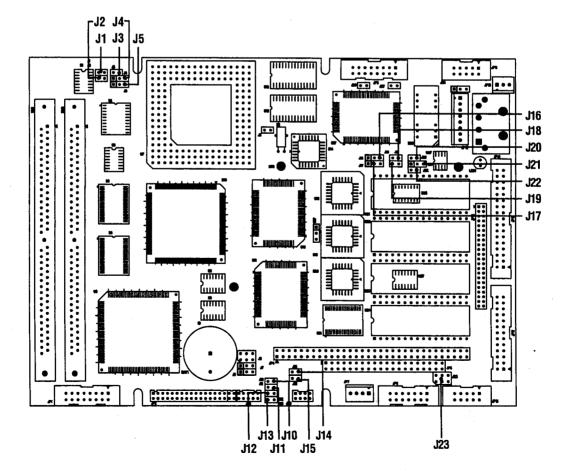
on page 7 gives an overall view of the card. also includes a function list for each of the card's jumpers. The figure This section identifies the location of the card's major components. It

Jumpers and connectors

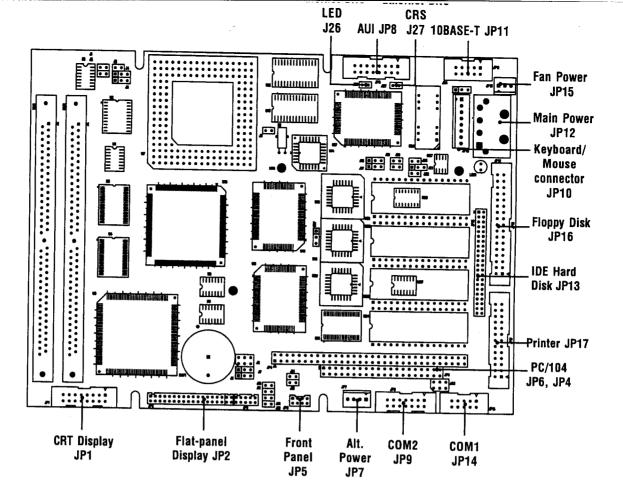
number of jumpers which you use to configure it for your application. drives, a keyboard or PC/104 modules. In addition, the board has a Connectors on the board link it to external devices such as hard disk

assignments for each connector appear in Appendix C. gives instructions for connecting external devices to your card. Pin connectors. Later sections in this chapter give instructions on setting jumpers and detailed information on each jumper setting. Chapter 3 The table below lists the function of each of the board jumpers and

Number J1 J2 J3 J4 J5 J10 J11 J12 J13 J14 J15 J16 J16 J17 J18 J18 J19 J19 J22 J22 J	CPU frequency selection CPU frequency selection CPU type selection CPU type selection CPU type selection CPU type selection SD memory address selection SSD memory address selection	
	CPU frequency selection CPU frequency selection CPU type selection CPU type selection CPU type selection SSD memory address selection SSD memory address selection	Page
	CPU frequency selection CPU type selection CPU type selection CPU type selection SSD memory address selection SSD memory address selection SSD I/O address selection	12
	CPU type selection CPU type selection CPU type selection SSD memory address selection SSD memory address selection SSD I/O address selection	12
	CPU type selection CPU type selection SSD memory address selection SSD memory address selection SSD I/O address selection	13
	CPU type selection SSD memory address selection SSD memory address selection SSD I/O address selection	13
	SSD memory address selection SSD memory address selection SSD I/O address selection	13
	SSD memory address selection SSD I/O address selection	16
	SSD I/O address selection	16
		17
	SSD I/O address selection	17
	SSD drive emulated	18
	SSD drive emulated	18
	Parallel port IRQ selection	13
	SSD Flash/ROM device selection	
	Watchdog timer enable/disable	18
	Watchdog timer invokes IRQ 15	18
	SSD 1 MR/4 MR ROM device celection	18 22 23
	COS - mort and monte agreement	18 22 23 18
J23 COM2 RS-232/RS-422/RS-485 selection	Watchdog timer invokes system reset	18 22 23 18 23



PCM-4860 Jumpers



PCM-4860 Connectors

SIMM memory modules JP16 **JP15** JP13 JP14 **JP12** JP11 J26 JP10 JP9 JP8 JP6 JP7 ЪР4 Р5 JP2 된 Number PCM-4860 Connectors Ethernet BNC LED Printer/parallel port connector Floppy connector CPU fan power connector Ethernet CRS LED RS-232 serial port IDE connector Main power connector (+5 V, +12 V) Keyboard and mouse connector RS-232/RS-422/RS-485 serial port Secondary power connector (-5 V, -12 PC/104 ISA-bus expansion Front panel connector PC/104 ISA-bus expansion Flat panel display connector CRT display connector 10BASE-T connector Function 10BASE-2/10BASE-5 AUI connector 5 34 င္ဟာ 37 40 36 3 8 36 37 37 40 37 40 3 6 40 39 39 Page

Install them as described in Appendix B.

Memory Module) sockets which hold the card's DRAM memory. On the left end of the card are the two 72-pin SIMM (Single In-line

memory chips for the card's Flash/ROM disk. Instructions for At the right of the card are three 32-pin sockets which hold the

Flash/ROM disk

installing memory chips appear in Appendix B

SBC Configuration

jumpers necessary for the operation of the PCM-4860. Separate and Ethernet controller. Jumpers relevant to SBC configuration are: sections will discuss the configuration of the display controller, SSD This section will explain the function and configuration of the

Number Function	Function	Page
J1	CPU frequency selection	12
J2	CPU frequency selection	12
ມ	CPU type selection	13
. J 4	CPU type selection	13
J5	CPU type selection	13
J16	Parallel port IRQ selection	13
J18	Watchdog timer enable/disable	22
J19	Watchdog timer invokes IRQ 15	23
J22	Watchdog timer invokes system reset	23
J23	COM2 RS-232/RS-422/RS-485 selection	14

CPU clock speed (J1, J2)

speeds for the PCM-4860 are 25, 33 or 66 MHz and can be selected as shown below: this prior to installing and applying power to the CPU board. Clock that the jumpers are configured for the correct CPU clock speed. Do (J1, J2). If you change the processor in the future you must ensure the CPU frequency. This is done by setting the clock generator jumper This board is fitted with a special IC which allows the user to choose

33.3 MHz (default)



_	l (C)	*		_	
O = Open	66 MHz	* 33 MHz	25 MHz	CPU Frequency	PCM-4860 CP
•= Closed	•	•	0	cy J1	PCM-4860 CPU frequency selection
* = Default	0	•	0	J2	selection
			•		
				,	

CPU type selection (J3, J4, J5)

shown below: of one two-pin jumper (13) and two three-pin jumpers (14, 5) as configuring the CPU type selection jumpers (J3, 4, 5). These consist 486DX and 486DX2. Setting the card for the right CPU involves This card supports three different types of CPU, the 80486SX,

486DX (default)



	The same of the sa			
CPU	CPU type	J3	J4	55
* 4861	486DX/DX2	•	1-2	1-2
486SX	SX	0	2-3	2-3
0 =	O = Open	•= Closed	* = Default	

Parallel port IRQ selection (J16)

via a two-pin jumper (J16) as shown below: BIOS setup. The PCM-4860 is configured as IRQ7 (IRQ5 optional) LPT1 and can be disabled or changed to LPT2 or LPT3 in the system The PCM-4860 supports one parallel port. The port is designated as

IRQ7 (default)

12

PCM-4860 P	PCM-4860 Parallel port IRQ	2	-
IRQ	1-2	2-3	
IRQ5	•	0	
* IRQ7	0	•	
O = Open	•= Closed	* = Default	

COM2 RS-232/RS-422/RS-485 selection (J23)

The PCM-4860 supports two serial ports, a primary (COM1) and a secondary (COM2) port. The primary port operates in RS-232 mode only, the secondary port can be configured to operate in RS-232, RS-422 or RS-485 mode. This is done via a six-pin jumper (J23) as shown below.

PCM-4860 N	PCM-4860 Mode selection	.		
Mode	1-2	3-4	5-6	
RS-232	•	0	0	
RS-422	0	•	0	,
RS-485	0	0	•	
O = Open	•= Closed	* = Default		

The IRQ and address range for both ports are fixed. However if you wish to disable the port or change these parameters later you can do this in the system BIOS setup. The table below shows the settings for the PCM-4860's ports.

PCM-486	PCM-4860 Serial port default settings	It settings	
Port	Address Range	Interrupt	Default
COM1	2E8~3F8	IRQ4	3F8
COM2	2E8~3F8	IRQ3	2F8

Solid State Disk Configuration

This section will explain the function and configuration of the jumpers necessary for the operation of the PCM-4860's solid state disk (SSD). The SSD is optional and can be utilized by inserting memory devices onto the board. Jumpers relevant to SSD configuration are:

PCM-4860 S	PCM-4860 SSD Jumpers	
Number	Function	Page
J10	SSD memory address selection	16
J11	SSD memory address selection	16
J12	SSD I/O address selection	17
J13	SSD I/O address selection	17
J14	SSD drive emulated	18
J15	SSD drive emulated	18
J17	SSD Flash/ROM device selection	18
J21	SSD 1 MB/4 MB ROM device selection	18
	The second secon	

The PCM-4860 features an internal Flash/ROM disk drive. This drive emulates a floppy disk drive by using solid-state memory chips (Flash or EPROM) to store programs and data instead of the magnetic particles on the mechanical drive's disk. The Flash/ROM disk offers much faster access times than a floppy or hard disk and greatly increased reliability in harsh environments.

The Flash/ROM disk works by modifying the BIOS INT-13 disk I/O routine on boot-up. The routine then translates read and write commands to the disk so that they will correctly access the memory chips. You don't need any special drivers. You simply set the drive to act as a DOS drive (e.g. A: or C:) and use standard DOS commands (COPY, DIR, etc.) to manipulate your data.

Before you use the Flash/ROM disk, you will need to enable it with the BIOS Chipset Features Setup Program as detailed in Chapter 4.

- 27C010 128 KB x 8 EPROM
- 27C040 512 KB x 8 EPROM
- 28F010 128 KB x 8 +12 V Flash Memory (AMD/INTEL)
- 29C010 128 KB x 8 +5 V Flash Memory (ATMEL only)
- 29C040 512 KB x 8 +5 V Flash Memory (ATMEL only)

If you use EPROM, files on the disk are read only. You will need an external programmer to load your program and data files on the EPROMs.

If you use +5 V Flash memories (29C010) for the solid state disk, you can read or write data just like a floppy disk; you need not use an external programmer. If you use +12 V Flash memories (28F010) you will still need an external programmer to write data.

Before you activate the Flash/ROM drive (using the BIOS Chipset Features Setup program), you will need to set the drive's I/O and memory addresses to avoid conflicts with other plug-in cards. You will also need to set the DOS drive designation to be used by the Flash/ROM drive. The following jumpers sets the configuration of the SSD as described in the following sections:

Memory address selection (J10, J11)

The SSD occupies a 8 Kbyte window in the upper memory address range of D6000 to D7FFF. You should ensure this does not conflict with any other device's memory address. Jumpers J10 and J11 control the Flash/ROM disk's memory address. If you select "Disabled", the disk will not function.

D6000 to D7FFF (default)



PCM-4860 SSD memory address	ddress		
Memory address (HEX)	J10	J11	
Disabled	0	0	ļ
DE000 to DFFFF	0		
D6000 to D7FFF	•	0	
$O = Open$ $\bullet = Closed$ * = Default	* = De	fault	

These addresses might conflict with the ROM BIOS on some of your other boards (i.e. PC/104 modules). Read the manuals for these modules to ensure there is no memory conflict.

I/O address selection (J12, J13)

Jumpers J12 and J13 control the disk's I/O address. The default is set to 2C0~2C4 but should be set as to not conflict with the I/O address of other devices. J12 and 13 are set as shown below:

2C0~2C4 (default)

	PCM-4860 SS	PCM-4860 SSD I/O address		
•	I/O address (HEX)	нех)	J12	J13
*	200-204		0	0
	240-244		0	
	280-284		•	0
	200-204		•	•
	O = Open	•= Closed	* = Default	

Drive emulated (J14, J15)

ROM disk as 1st, 2nd, 3rd or 4th as shown below: Jumpers J14 and J15 control the DOS drive emulated by the Flash

3rd Drive (default)



	PCM-4860 SS	PCM-4860 SSD drive designation	nation	
	Drive	J14	J15	
	1st	•	•	
	2nd	0	•	
*	3rd	•	0	
	4th	0	0	
	O = Open	•= Closed *= Default	* = Default	

DOS version. depends on the floppy or hard disks installed in the system and the The actual drive letter assigned by DOS to the Flash/ROM disk

SSD device and size selection (J17, J21)

will have $3 \times 512 \text{ KB} = 1.5 \text{ MB}$, equivalent to a 1.44 MB floppy. The chips you install. For example, if you install three 512 KB chips, you 360 KB, 720 KB, 1.2 KB and 1.44 floppy drives following table shows the memory chips you will need to emulate The size of the emulated drive depends on the size and number of the

devices must be the same type and size. ROM) and size (128 KB or 512 KB) of the devices you use. All the You will need to set jumpers J17 and J21 to match the type (Flash or

EPROM 1.44 MB (default)



settings of jumpers J17 (Flash/ROM) and J21 (size 128 KB/512 KB) need for each size emulated disk. It also shows the corresponding The following table shows the size and number of devices you will

ection OKB	7 	1-4860 SSD device J21 Device	1-2 1-2 ROM 512KBx1 512KBx2 512KBx3 512KBx3	1-2 2-3 ROM 128KBx3 — -		2-3 2-3 Flash 128KBx3
and sele	and selection B 720KB	vice type				
	ection OKB	and sele	Rv1 51	0	(Bx3 —	(Bx3 —
1.2MB 512KBx3		1.44MB	512KBx		1	1 1

Floppy disks

You will not be able to access the floppy drive. drive operations directed at drive A: will go to the Flash/ROM disk Flash/ROM disk to be the 1st drive (both J14 and J15 closed), any example, if you have a single floppy disk (drive A:) and assign the The Flash/ROM disk will replace the corresponding floppy disk. For

Hard disks

hard disks, the Flash/ROM drive will become drive E:. open), the Flash/ROM drive will become drive D:. If you have two and assign the Flash/ROM disk to be the 3rd drive (J14 closed, J15 designation. For example, if you have a single hard disk (drive C:) The Flash/ROM disk will not replace corresponding hard disks. Instead, DOS will assign the Flash/ROM disk to the next free drive

Floppy disks

The Flash/ROM disk will replace the corresponding floppy disk. For example, if you have a single floppy disk (drive A:) and assign the You will not be able to access the floppy drive. drive operations directed at drive A: will go to the Flash/ROM disk Flash/ROM disk to be the 1st drive (both J14 and J15 closed), any

disk will become drive E: drive C:, the first hard disk will become drive D: and the second hard D:. If you have two hard disks, the Flash/ROM drive will become ROM drive will become drive C: and the hard disk will become drive Flash/ROM disk to be the 3rd drive (J14 closed, J15 open), the Flash/ example, if you have a single hard disk (drive C:) and assign the DOS will assign the hard disk to the next available drive letter. For The Flash/ROM disk will take the drive letter of the hard disk and

Before installing Flash/ROM disk

DOS 3.3	DOS 5.0	
FDD	FDD	A
FDD	FDD	8
HDD	HDO	C

After installing Flash/ROM disk

DOS 5.0	
FDD	A
FDD	æ
HDO	C
Flash/ROM	D
	FDD FDD HDD I

Booting from the Flash/ROM disk

start the system, it will boot from the solid state disk. boot (command.com, io.sys, autoexec.bat, etc). The next time you tion files to the disk along with the standard system files required to set both J14 and J15 closed to select the 1st FDD. Copy your applica-If you wish to have the system boot from the Flash/ROM disk, simply

Inserting memory devices

will need to program EPROMs before you insert them priate memory devices into the card's sockets. Remember that you After you've set all the jumpers on the PCM-4860, insert the appro-

1. Make sure that the pins of the memory chips are perpendicular to the case and both rows are parallel to each other. Often the chips

> directly down. top and carefully bend each line of pins together until they point come with the pins spread out slightly. Place the chip on a table

a gap between the chip body and the socket when it is fully seated matches the notch on the end of the socket. There will probably be the connector and the semicircular notch on the end of the chip Insert each chip. Align the chips so their pins are perpendicular to Do not push too hard!.



Using a memory manager (EMM386.EXE)

addresses used by the Flash/ROM disk (set by jumpers J10 and J11) causing unreliable operation Otherwise, the memory manager will attempt to use these addresses. EMM386 or QEMM386), you will need to configure it to avoid the If you are using an extended or expanded memory manager (such as

EMM386, the DOS memory manager, might be the following: For example, the line in your CONFIG.SYS file that invokes

DEVICE=EMM386.SYS X=D600-D7FF

default addresses). This excludes a 8 KB range for the card from D6000 to D7FFF (the

addresses. For example, the memory manager is not putting the page frame in the disk's If you are using expanded memory, you will need to make sure that

DEVICE=EMM386.EXE X=D600-D7FF FRAME = D800

shadowed in the BIOS You should also make sure that the disk's memory address is not

VGA Display Configuration

completely via the software utility, you don't have to set any jumpers analog CRT monitors. Configuration of the VGA interface is done Refer to chapter 2 for software setup details. popular LCD, EL, gas plasma flat-panel displays and traditional The PCM-4860's on-board VGA interface supports a wide range of

Ethernet Configuration

systems and is 100% Novell NE2000 compatible. As with the VGA See chapter 2 for software setup. interface, configuration is very simple and is via the software utility. CD standards. This card is supported by all major network operating Interface which is fully compliant with IEEE 802.3 10 Mbps CSMA The PCM-4860 is equipped with a high performance 16-bit Ethernet

Watchdog Timer Configuration

software to control it (refer to Appendix A). The two jumpers are: tions. Setting up the watchdog involves two jumpers and running the invaluable protective device for stand-alone or unmanned applica-EMP (electro-magnetic pulse) interference can cause. This is an An on-board watchdog timer reduces the chance of disruptions which

Watchdog timer – enabled/disable (J18)

information on programming the watchdog timer see Appendix A. jumper (J18) as shown below: Configure the watchdog timer to be enabled or disabled via a two-pin refresh the watchdog or address 043 to disable the watchdog. For I/O ports with your program. Read address 443 hex to enable and You can enable or disable the watchdog timer by reading the card's

Enabled (default)



Enabled O = OpenDisabled Watchdog timer PCM-4860 Watchdog timer •= Closed <u>1</u>8 0 * = Default

system reset/IRQ15 (J19, J22) Watchdog timer -

can be set via two two-pin jumpers (J19, J22) as shown below: halt), it can reset the system or generate an interrupt on IRQ15. This When the watchdog timer activates (CPU processing has come to a

System reset (Default)



PCM-4860 Wa	PCM-4860 Watchdog timer	er e
Watchdog timer	ier J19	J22
IRQ15	•	0
System reset	0	•
O = Open	•= Closed	Closed * = Default

For information on programming the watchdog timer see Appendix A.

Check card default settings

skip to step 3. The default configuration is as follows: configuration. If this configuration matches your needs, you can We set the card's jumpers at the factory for the most popular

SBC defaults

- CPU frequency: 33 MHz (J1, J2)
- CPU type: 486DX (J3, J4, J5)
- Parallel port IRQ: IRQ7 (J16)
- COM2 port RS-232/422/485: RS-232 (J23)
- Watchdog timer enabled/disabled: Enabled (J18)
- Watchdog invokes IRQ15/system reset: System reset (J19, J22)

SDD defaults

- SSD memory address: D6000~D7FFF (J10, J11)
- SSD I/O address: 2C0~2C4 (J12, J13)
- SSD drive designation: 3rd (J14, J15)
- SSD device/size: EPROM/512kx3/1.44 MB (J17, J21)

Set jumpers

setting. If you need more information, just check the appropriate page references. Check the figure on page 7 for the location of Jumpers or connectors. This section gives a quick description of each card configuration

CPU frequency J1, J2 — page 12

Sets the PCM-4860's system clock frequency:

CPU Frequency J1 J2 25 MHz O O * 33 MHz ● ● 66 MHz ● O	PCM-4860 CPU frequency selection	frequency s	selection	-
66 33 25	CPU Frequency	J	J2	
ရွာမျှ		0	0	The state of the s
66 MHz	* 33 MHz	•	•	
The state of the s	66 MHz	•	0	

 $O = Open \quad \bullet = Closed \quad * = Default$

CPU type J3, J4, J5 — page 13

Sets the boards configuration to correct CPU type

O = Onen	486SX O	* 486DX/DX2 ●	CPU type J3	PCM-4860 CPU type
* I Default	2-3 2-3	1-2 1-2	J4 J5	

Parallel port J16 — page 13

Sets the IRQ for the parallel port:

	PCM-4860 Parallel port IRQ	port IRQ	
	IRQ	1-2	2-3
	IRQ5	•	0
*	IRQ7	0	•

O = Open•= Closed * = Default

The second of th

selection J23 — page 14 COM2 port RS-232/422/485

Selects the mode of operation for the serial port (COM2):

	PCM-4860 M	PCM-4860 Mode selection	-	
	Mode	1-2	3-4	5-6
*	RS-232	•	0	0
	RS-422	0	•	0
	RS-485	0	0	•
	O = Open	•= Closed	* = Default	

disable J18 — page 22 Watchdog timer enable/

Enables or disables the cards watchdog timer:

PCM-4860 Watchdog timer	chdog time	¥ .		
Watchdog timer	r J18			
Enabled	•			
Disabled	0			
O = Open	•= Closed	* = Default	•	·

IRQ15 J19, J22 — page 23 Watchdog invokes IRQ15/system reset:

Sets the watchdog timer to produce a system reset or an IRQ15:

PCM-4860 W	PCM-4860 Watchdog timer	٦
Watchdog timer	1er J19	J22
IRQ15	•	0
System reset	0	•
O = Open	•= Closed	* = Default

SSD memory address: D6000~D7FFF J10, J11 — page 16

Sets the memory address for the solid state disk:

	PCM-4860 SSD memory address	ddress	
	Memory address (HEX)	J10	J11
	Disabled	0	0
	DE000 to DFFFF	0	•
*	D6000 to D7FFF	•	0
	CE000 to CFFFF	•	
		,	

SSD I/O address: 2C0~2C4 J12, J13 — page 17

Sets the I/O address for the solid state disk:

≥	N	Ŋ	* 2	.	סו	ı
200-204	280-284	240-244	200-204	/O address (HEX)	PCM-4860 SSD I/O address	
				HEX)	0/10	
					ado	ļ
					ress	
•	•	0	0	J12		
•	0	•	0	J13		

$O = Open \quad \bullet = Closed \quad * = Default$

SSD drive designation: 3rd J14, J15 — page 18

Sets the drive designation for the solid state disk:

PCM-4860	PCM-4860 SSD drive designation	nation	
Drive	J14	J15	
1st	•	•	
2nd	0	•	
· 3rd	•	0	
4th	0	0	
O = Open	•= Closed	* = Default	

EPROM/512kx3/1.44 MB J17, J21 — page 18 SSD device/size:

Sets the solid state disk's device type, memory type and emulated drive size:

			*		
2-3	2-3	1-2	1-2	J17	PCM-
1-2	2-3	2-3	1-2	J21	4860 S
EPROM	EPROM	ROM	ROM	Device	SD device
EPROM 512KBx1	128KBx3	128KBx3	512KBx1	360KB	PCM-4860 SSD device type and selection
512KBx2 512KBx3	1	1	512KBx2	720KB	selection
512KBx3	1	!	512KBx3	1.2MB	
512KBx3	1	1	512KBx3	1.44MB	

Ņ Install CPU

If your CPU was not installed, install it. See Appendix B.

Install memory

If your memory was not installed, install it. See Appendix B.

Ģ install card

system. Install the card into your system in a manner appropriate to your

Please observe the following precautions:

Warning! Disconnect all power from the chassis before don't just turn off the power switch. chassis. Unplug the power cord from the wall down procedure outlined in the manual for your you install the PCM-4860. Follow the power-

Caution! The PCM-4860's components are very sensitive strap to discharge all static electricity before you bag when they are not in the chassis. a static-dissipative surface or static-shielded touch any components. Place all components in to static electric charges. Use a grounding wrist

<u>ე</u> Connect peripherals

connector pin assignments appear in Appendix C complicated connections appear on the following pages. Other references the detailed description in the text. Details for the more for help finding connectors. The page number after each connector Connect the following external devices. See the figure on page 7

PCM-4860 Connectors	onnectors	
Number	Function	Page
JP1	CRT display connector	39
JP2	Flat panel display connector	39
JP4	PC/104 ISA-bus expansion	40
JP5	Front panel connector	36
JP6	PC/104 ISA-bus expansion	40
JP7	Secondary power connector (-5 V, -12 V) 37	37
JP8	10BASE-2/10BASE-5 AUI connector	40
JP9	RS-232/RS-422/RS-485 serial port	38
JP10	Keyboard and mouse connector	36
JP11	10BASE-T connector	40
JP12	Main power connector (+5 V, +12 V)	37
JP13	IDE connector	35
JP14	RS-232 serial port	37
JP15	CPU fan power connector	37
JP16	Floppy connector	34
JP17	Printer/parallel port connector	36
J26	Ethernet BNC LED	•
J27	Ethernet CRS LED	1

Set up the System BIOS

Apply power and setup the System BIOS as detailed in Chapter 4.

œ Set up the VCA and Ethernet BIOS

differently from the default settings. Refer to Chapter 2. Setup the VGA and Ethernet BIOS if your system is to operate

the chance of bending pins or causing other damage. Next, probably be a gap between the CPU and the connector when it notch on the corner of the CPU matches with the notch on the carefully align the CPU so it is parallel to the socket and the is fully seated – Do not push too hard! This will make the new CPU slide in MUCH easier and reduce inside of the socket. Carefully slide in the CPU. There will

4 Switch jumpers J1, J2 (CPU speed) and J3, J4, J5 (CPU type selection) to match the new CPU's according to the following

	PCM-4860 CPU	CPU and freq	frequency selection	election		
	CPU type	<u>-</u>	J2	ភ	J4	
	SX-25	0	0	0	2-3	ယ
	SX-33	•	•	0	2	2-3
	DX-25	0	0	•		1-2
~	DX-33/DX2-66	•	•	•	1	1-2
	DX2-50	0	0	•	_	1-2
	O = Open	Closed	sed	* = Default		

Installing PC/104 modules

and valuable slots. Modules include: attach PC/104 expansion modules. These modules perform the functions of traditional plug-in expansion cards, but save space The PCM-4860's PC/104 connector gives you the flexibility to

- PCM-3110 PCMCIA Module
- PCM-3111 Secondary PCMCIA Module
- PCM-3610 Isolated RS-232 and RS-422/485 Module
- PCM-3640 Four port RS-232 Module
- PCM-3718 30 KHz A/D Module
- PCM-3724 48-channel DIO Module
- · PCM-3910 Breadboard Module

operation. The following steps show how to mount the PC/104 To install these modules on the PCM-4860 is a quick and simple

- Step 1 Remove the PCM-4860 from your system or make it instructions already mentioned above. accessible, paying particular attention to the safety
- Step 2 Make any jumper or link changes required to the you may have difficulty in accessing these. PCM-4860 now. Once the PC/104 module is mounted
- Step 3 Mount the PC/104 module onto the PCM-4860. Do this mounting connectors. by pressing the module firmly but carefully onto the
- Step 4 the four mounting spacers and screws. Secure the PC/104 module onto the PCM-4860 using

CRT display connector (JP1)

Flat-panel display connector (JP2)

PCM-486	PCM-4860 Flat-panel display connector	lay connector	7
Pin	Signal	Pin	Signal
1	FLM	2	ACDCLK
ယ	LP	4	V _{CC}
5	ENABKL	6	SIGNAL GND
7	P10	8	SIGNAL GND
9	P11	10	SIGNAL GND
11	P12	12	SIGNAL GND
13	P13	14	SIGNAL GND
15	P8	16	SIGNAL GND
17	P7	18	SIGNAL GND
19	P6	20	SIGNAL GND
21	P5	22	SIGNAL GND

Pin	Signal	Pin	Signal
23	P3	24	SIGNAL GND
25	P2	26	SIGNAL GND
27	P1	28	SIGNAL GND
29	P0	30	SIGNAL GND
31	SHFCLK	32	SIGNAL GND
33	P4	34	P14
35	P15	36	P9
37	ENAVEE	38	SIGNAL GND
39	+12 V	40	+12 V
41	-12 V	42	SIGNAL GND
43	VCC	44	SIGNAL GND

Front panel connector pin assignments for supported displays

PCM-4860 JP2	60 JP2	Toshiba LTM09C015A	109C015A	Sharp	LM64P8X/837
Pin#	Function	Pin #	Function	Pin #	Function
29	PO	•	•	=	DU3
27	Р1	4		10	DU2
25	P2	CN2-1	В0	9	DU1
23	Р3	CN2-3	В1	8	DUO
ဒ္	P4	CN2-5	82	15	DL3
21	P5	•	,	14	DL2
19	P6	1	•	13	DL1
17	P7	CN1-9	60	12	DL0
15	P8	CN1-11	G1	•	•
36	P9	CN1-13	G2	•	1
7	P10	•	4	1	•
9	P11	1	1	ı	1
11	P12	CN1-3	R0	•	•
13	P13	CN1-5	R1		•
34	P14	CN1-7	R2	4	•

The second secon

	Pin	Signal (JP4)		Signal (JP6)	
	Number	RowA	RowB	RowA	RowB
	17	SA14	DACK1	SD14	MASTER
	18	SA13	DRQ1	SD15	۷0
	19	SA12	REFRESH	(KEY)	0
	20	SA11	SYSCLK	1	;
	21	SA10	IRQ7	1	1
	22	SA9	IRQ6	•	
•	23	SA8	IRQ5	3	1
	24	SA7	IRQ4		-
	25	SA6	IRQ3	•	1
	26	SA5	DACK2	:	1
	27	SA4	TC	•	1
	28	SA3	BALE	1	1
	29	SA2	+5V		:
	30	SA1	OSC	8	-
	31	SA0	۷0	•	:
	32	۷۷	۷0	•	1

PC/104 Connector (JP4)

ENAVEE VEESAFE

٧EE

6, 8, 10, 12, 14, 16, 18, 20, 24, GND 26, 28, 30, 32 37 ENAV

CN1-2, 4, 6, 8, 10 12 CN2-2, 4, 6, 8

VSS

Pin # Function

Pin #

Function Pin # Function

CN1-1

NCLK

5 ACDCLK FLM SHFCLK

CN2-7

ENAB

လ CP2

CN2-9/10 V_{cc}

4,5

CP1 VDD, DISP

PCM-4860	PCM-4860 PC/104 connector pin assignments	ector pin assi	gnments	
Pin	Signal (JP4)		Signal (JP6)	
Number	RowA	RowB	RowA	RowB
0	;	•	۷0	۷0
_	IOCHCHK	۷0	SBHE	MEMCS16
2	SD7	RESETDRV	LA23	10CS16
ω	SD6	+5V	LA22	IRQ10
4	SD5	IRQ9	LA21	IRQ11
ς ₁	SD4	-5V	LA20	IRQ12
6	SD3	DRQ2	LA19	IRQ15
7	SD2	-12V	LA18	IRQ14
8	SD1	ENDXFR	LA17	DACK0
9	SD0	+12	MEMR	DRQO
10	IOCHRDY	(KEY)	MEMW	DACK5
1	AEN	SMEMW	SD8	DRQ5
12	SA19	SMEMR	SD9	DACK6
13	SA18	WOI	SD10	DRQ6
14	SA17	IOR	SD11	DACK7
15	SA16	DACK3	SD12	DRQ7
16	SA15	DRQ3	SD13	85V

Front panel connector (JP5)

RESET SWITCH+	8
RESET SWITCH- (GND)	7
WATCHDOG TIMER OUT	6
GND	5
SPEAKER- (GND)	4
SPEAKER+	ယ
LED- (GND)	2
LED+ (HARD DISK ACTIVE)	_
Signal	Pin
PCM-4860 Front panel connector	PCM-48

AUI Connector (JP8)

16	15	14	13	12	11	10	9	8	7	6	5	4	ဒ	2	_	Pin	PCM-4860
+5 V	N/C	N/C	N/C	GND	GND	+12 V	Rx+	Rx-	GND	GND	Tx+	Tx-	CD+	CD-	GND	Signal	PCM-4860 10BASE-2/5 AUI connector

RS-232/422/485 Serial port (JP9)

Pin	Signal	
9	GND	
10	N/C	•
<u>-</u>	485 TxD+	
12	485 TxD-	
13	485 RxD+	
14	485 RxD-	

Keyboard and mouse connector (JP10)

PCM-48	PCM-4860 Keyboard and mouse connector
Pin	Signal
_	GND
2	MS V _{cc}
ယ	MS DATA
4	MS CLOCK
SI	GND
6	KB V _{cc}
7	KB DATA
8	KB CLOCK

10BASE-T connector (JP11)

PCM-48	PCM-4860 10BASE-T connector	
Pin	Signal	
_	V_{cc}	
2	CRS LED	
2	RCV+	
4	RCV-	
5	BNC LED	
6	GND	
7	N/C	
&	GND	
9	XMT+	
10	TMX-	

IDE Connector (JP13)

43	41	39	37	35	33	31	29	27	25	23	21	19	17	15	13	11	9	7	5	ω	_	Pin	PCM-
GND	VCC	IDE ACTIVE	HARD DISK SELECT 0	ADDR 0	ADDR 1	IRQ14	N/C	10 CHANNEL READY	IO READ	IO WRITE	N/C	SIGNAL GND	DATA 0	DATA 1	DATA 2	DATA 3	DATA 4	DATA 5	DATA 6	DATA 7	IDE RESET	Signal	PCM-4860 IDE Connector
44	42	40	38	36	34	32	30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	Pin	
N/C	MVCC	MGND	HARD DISK SELECT 1	ADDR 2	N/C	10CS16	GND	ALE	GND	GND	GND	N/C	DATA 15	DATA 14	DATA 13	DATA 12	DATA 11	DATA 10	DATA 9	DATA 8	GND	Signal	

RS-232 Port connector (JP14)

PCM-48	PCM-4860 RS-232 Port connector	,
Pin	Signal	
_	DCD	
2	DSR	
ယ	RxD	
4	RTS	
თ	TxD	
6	CTS	
7	DTR	
8	RI	
9	GND	
10	N/C	

Floppy drive connector (JP16)

33	31	29	27	25	23	21	19	17	15	13	=	9	7	თ	ω	_	Pin	PCM-48
GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	Signal	PCM-4860 Floppy drive connector
34	32	30	28	26	24	22	20	18	16	14	12	10	8	6	4	2		nnector
DISK CHANGE	HEAD SELECT	READ DATA	WRITE PROTECT	TRACK 0	WRITE GATE	WRITE DATA	STEP	DIRECTION	MOTOR 1	DRIVE SELECT 2	DRIVE SELECT 1	MOTOR 0	INDEX	DRIVE TYPE	N/C	DENSITY SELECT		

Parallel/printer port connector (JP17)

N/C	26
SLCT	25
GND	24
PE	23
GND	22
BUSY	21
GND	20
VACK	19
GND	-1 8
07	17
GND	16
D6	15
GND	14
D5	13
GND	12
D4	=
GND	10
D3	9
\SLCTINI	∞
D2	7
TINIT	6
D1	5
ERR	4
D0	ယ
\AUT0FD	2
\STROBE	-
Signal	Pin
PCM-4860 Floppy drive connector	PC

Optional Extras

