

SIMB-M01**Intel® Atom D525 Mini-ITX Motherboard****Startup Manual****Packing List**

Before you begin installing your card, please make sure that the following items have been shipped:

- 1 x Intel D525 mini-ITX Main board
- 1 x CD ROM per carton, which contains the followings:
 - User's Manual in PDF file
 - Drivers
- 1 x SATA cable kit (SATA/Power)
- 1 x I/O Shield
- 1 x Startup Manual per carton

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

Note1:

Acrobat Reader is required to view any PDF file. Acrobat Reader can be downloaded at: www.adobe.com/Prodindex/acrobat/readstep.html (Acrobat is a trademark of Adobe).

Specifications**System**

- **CPU:** Onboard Intel® Atom D525 DC 1.8GHz Processor
- **BIOS:** AMI 8Mb SPI
- **System Chipset:** Intel® NM10
- **I/O Chipset:** FINTEK F71869ED
- **Memory:** Two 204-pin SO-DIMM sockets support up to 4GB Single Channel DDR3 800 MHz SDRAM
- **H/W Status Monitor:** Monitoring temperature, voltage and cooling fan status. Auto throttling control when CPU overheats
- **Expansion Slots:** 1 PCI-E x 1
- **Power State:** S1, S3, S4, S5
- **Wake up on LAN or Ring:** LAN (WOL) and Ring (WO)
- **Smart Fan Control:** Yes

Display

- **Chipset:** Integrated Intel® GMA Gen3.5 DX9, 400 MHz
- **Display Memory:** Shared Memory Max 224MB
- **Dual Display:** VGA + HDMI
- **VGA:** Onboard, supports max resolution 2048 x 1536 (@60Hz)
- **HDMI:** Onboard HDMI 1.3, supports max resolution 1920 x 1080

Audio

- **Audio Codec:** Realtek ALC662, 5.1 CH. HD Audio
- **Audio Interface:** Line-in, Line-out, Mic.-in, S/PDIF, Front Audio Header

Ethernet

- **LAN1:** Realtek 8111E Gigabit Ethernet

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For technical support and service, please visit our support website at:


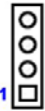
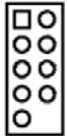
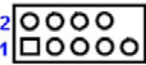
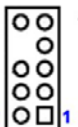

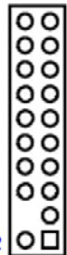
<http://www.advantech.com/support>

This manual is for the SIMB-M01 series Rev. A1.

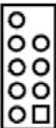
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


1st Edition
July 2011

Connectors and Jumpers

Connectors		
Label	Function	Note
CPU_FAN1	CPU Fan Connector	 <ul style="list-style-type: none"> 1. GND 2. +12VPWM 3. Sensor
SYS_FAN1	System Fan Connector	 <ul style="list-style-type: none"> 1. GND 2. +V12 3. FANCPUDEC1 4. FAN_PWM1_C
Serial_B	Serial Port Connector	 <ul style="list-style-type: none"> 1. DDCD2# 2. RRXD2 3. TTXD2 4. DDTR2# 5. GND 6. DDSR2# 7. RRTS2# 8. CCTS2# 9. RRI2
F_PANEL	Intel Front Panel connector	 <ul style="list-style-type: none"> 1. HDD_LED+ 2. SUPLED 3. SATA_LED# 4. GND 5. GND 6. PANSWIN# 7. SRST# 8. GND 9. NC
F_AUDIO	Audio Mic.-In & Line-Out Connector	 <ul style="list-style-type: none"> 1. MIC2_L 2. GND 3. MIC2_R 4. PRESENSE 5. LIN2_R 6. SENSE1_RTN 7. SENSE_B 8. NC 9. LIN2_L 10. SENSE2_RTN
SPDIF_OUT	SPDIF OUT	 <ul style="list-style-type: none"> 1. +5V 3. SPDIF_O 4. GND
TPM	TPM Connector	 <ul style="list-style-type: none"> 1. CK_33M 2. GND 3. LFRAME# 4. PLTRST# 5. LAD3 6. SMB_DATA 7. LAD2 8. LAD1 9. 3V 10. LAD0 11. NC 12. GND 13. NC 14. SERIRQ 15. 3VSB 16. CLKRUN# 17. GND 18. SMB_CLK 19. LPCPD# 20. LAD0

Connectors and Jumpers

F_USB1 F_USB2	USB 2.0 Connector	 <div><div>9. NC 7. GND 5. USB + 3. USB - 1. USB+5V</div><div>8. GND 6. USB + 4. USB - 2. USB+5V</div></div>
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Jumpers		
Label	Function	Note
JCMOS1	Clear CMOS 	<div><div>Normal*</div></div> <div><div>Clear CMOS</div></div>

Disclaimer and Notice

The manufacturer reserves the right to make changes, without notice, to any product, including circuits and/or software described or contained in this manual in order to improve design and/or performance. The manufacturer assumes no responsibility or liability for the use of the described product(s), conveys no license or title under any patent, copyright, or masks work rights to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified. For the detail product information, please refer to user's manual.

Safety Declaration

This device complies with the requirements in Part 15 of the FCC rules. Operation is subject to the following two conditions:

1. This device may not cause harmful interference.
2. This device must accept any interference received, including interference that may cause undesired operation.

Board Diagram

